

ATENEO DE MANILA UNIVERSITY
 Loyola Schools
 Course Syllabus for 1st Semester, School Year 2013-2014

Department	ECCE	School	SOSE
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Course No.	CE 180
Course Title	PROGRAMMING LANGUAGES FOR ENGINEERS
No. of Units	3

Course Description:

This is an advanced digital design system course. This course covers hardware programming language – Verilog HDL. This course covers an introduction to Field Programmable Gate Array (FPGA).

Course Objective/s:

To have a hands-on knowledge of Verilog HDL and its applications.
To have a hands-on knowledge of FPGA and its applications.

Course Outline:

- I. ***Introduction to FPGA***
- II. ***Introduction to hardware programming languages.***
- III. ***Introduction to Verilog HDL***
- IV. ***Behavioral modeling using Verilog HDL***
- V. ***Sequential processing using Verilog HDL***
- VI. ***Verilog HDL synthesis***
- VII. ***Verilog HDL Case Studies and Applications***
- VIII. ***FPGA Applications***

References:

Michael Ciletti, Modelling, Synthesis, and Rapid Prototyping with the Verilog HDL, Prentice Hall New Jersey 1999.
Andrew Rushton, Verilog for Logic Synthesis 2nd Ed., John Wiley & Sons 1999
Stephen Brown & Zvonko Vranesic, Fundamentals of Digital Logic with Verilog Design, McGraw-Hill International Edition, 2008.
M. Morris Mano, Digital Design Third Edition, Prentice Hall, Inc. 2002

Course Requirement:

Class Work	90%
long tests, programming assignments, mini=projs, 1 project	
Class participation	10%
class conduct, recitation, board work, assignments and problem sets	

Grading System:

A	92+	B+	87-91+	F	below 59+
B	81-86+	C+	76-80+		
C	70-75+	D	60-69+		

Other Matters:

CLASSROOM POLICIES

1. Class attendance is always checked (by the beadle).
2. Students are allowed 6 cuts. Exceeding the allowable number of cuts automatically merits a grade of W for the course.
3. Maintain an atmosphere of an academic classroom.

Mailing List

An optional mailing list (or e-group) shall be established for the course. Various materials may be distributed thru the mailing list. While membership in the mailing list is not a requirement of the course, students who do not join the mailing list are responsible for material(s) distributed thru the mailing list.

Consultation Info:

T 10:30 to 1:30 or W 10:30 to 1:30 or by appointment or via text or email

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